

METHODS AND APPARATUS FOR TRISTATE LINE SHARING

Abstract of the Disclosure

5 Methods and apparatus are provided for interconnecting on-chip components,
such as components on a programmable chip, with off-chip components through a
variety of buses, fabrics, and input/output lines. Interconnection resources such as
input/output lines are shared for communication with different off-chip components
such as memory. Control circuitry and an arbitration fabric are provided to further
10 improve communication efficiency between on-chip and off-chip components.